

Fig. 1

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1/8  
109740-68451860

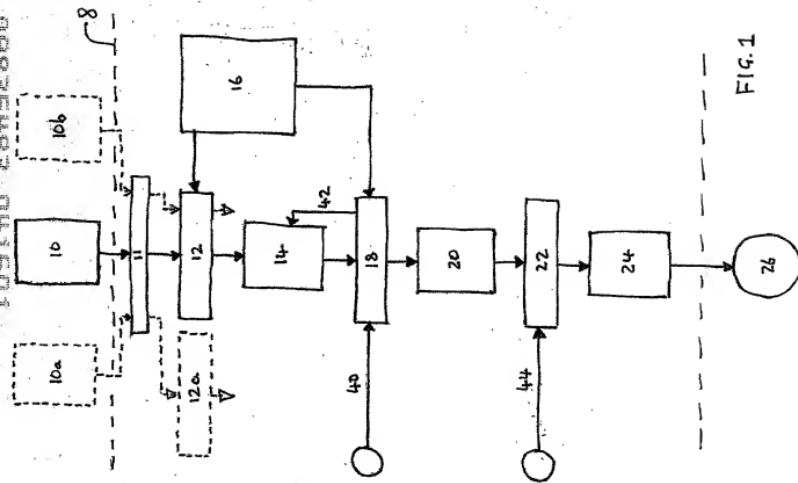
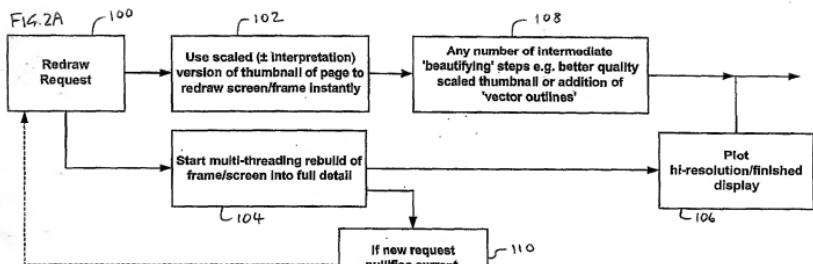


FIG. 1

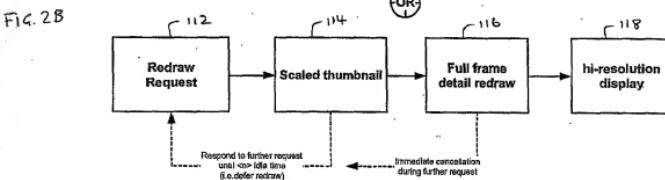
000333483 "041601



CO. DATA CODE: 000333483 - 041601 - 1

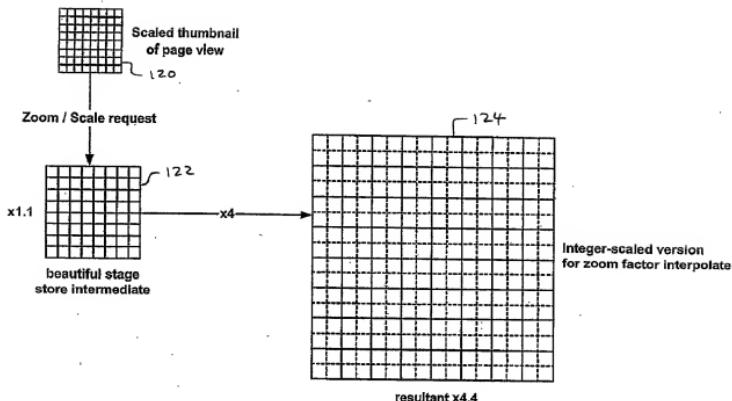
8|2

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FIG. 3

Versus

- Intermediate stage 'infrequent' & therefore can use beautiful/detailed scaling, versus rapid/crunder final or single stage scale.

FIG. 4-A

## non-tilted block copy

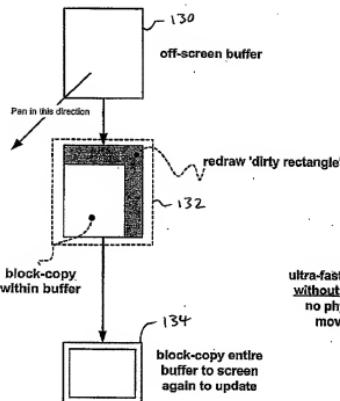
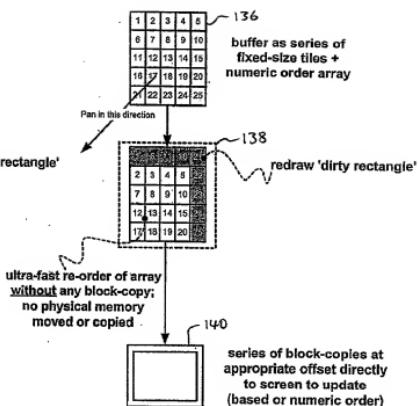


FIG. 4-B

## tilted cache



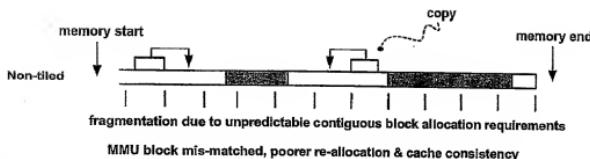
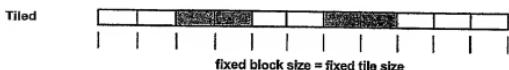


FIG. 5A

 = unused (released) blocks

 = large numbers of physical

Physical memory MMU

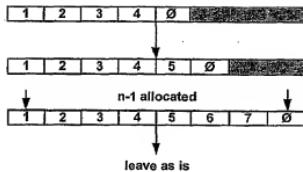
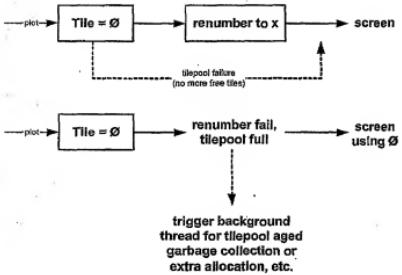


unlimited amount of fragmentation has no effect on usability  
no copy operations required (for buffer re-centering Fig.24)  
potential perfect synchronisation with MMU predictability  
& extendibility of pool (I)

FIG. 5B

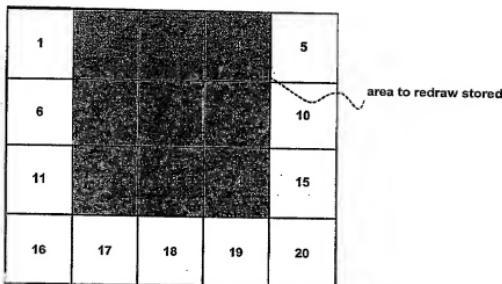
三九三五三三〇四一六〇三

FIG. 5C



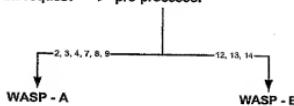
19  
/ 8

FIG. 6



tiles 1-10 handled by WASP-A  
tiles 11-20 handled by WASP-B

redraw request → pre-processor



-OR-

based on memory map of Fig. 5B  
split pool accordingly regardless of  
screen position

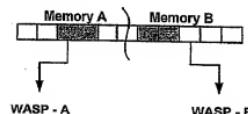


FIG. 7

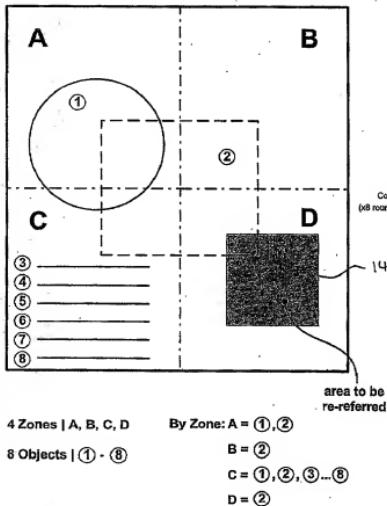
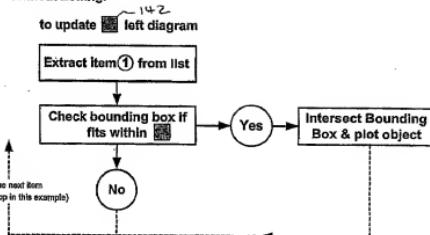


FIG. 8

Without Zoning:



With Zoning:

